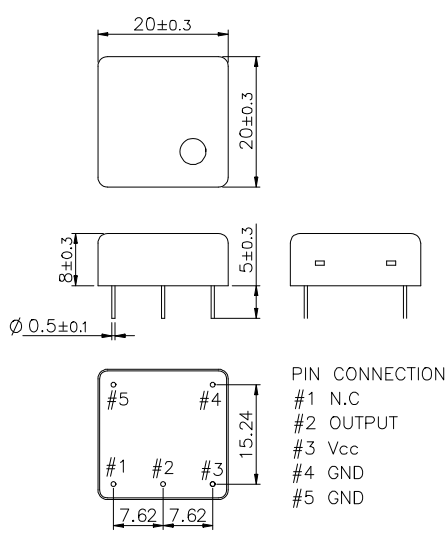
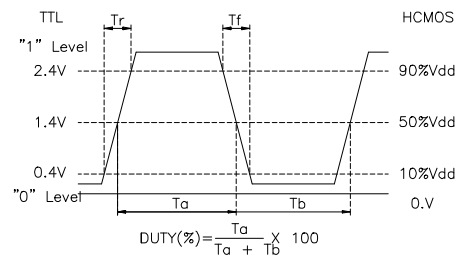
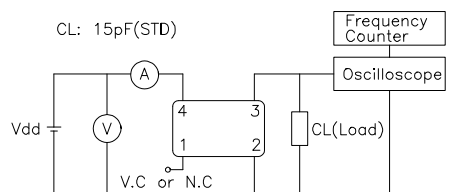


MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION																													
 <p>PIN CONNECTION</p> <ul style="list-style-type: none"> #1 N.C #2 OUTPUT #3 Vcc #4 GND #5 GND 	<p>Frequency range</p> <p>1.000KHz to 800.000MHz All combination of Frequency range Vs. Package type might not be available ,please contact factory.</p>																													
	<p>Frequency Stability</p> <p>vs. Temperature ±0.5 ppm to ±5.0ppm vs. Supply Voltage ±0.1 / ±0.3 ppm max / Vdd ± 5% vs. Load ±0.2 ppm max /15pF ±10% vs. Aging ±1.0 ppm max/ year</p>																													
	<p>Temperature Range</p> <p>Operating See Table 2 Storage -55℃ to 125℃</p>																													
	<p>Supply Voltage</p> <p>3.3V ± 5% 5.0V ± 5%</p>																													
	<p>Input Current</p> <p>3.3 V , 5V</p> <p>1.000KHz ~ 40.000MHz ~ 800.000MHz 15mA max ~ 30mA max ~ 50mA max</p>																													
<p>OUTPUT WAVEFORM</p> 	<p>Output characteristics</p> <table border="1"> <thead> <tr> <th></th> <th>HCMOS</th> <th>TTL</th> </tr> </thead> <tbody> <tr> <td>Logic "1"</td> <td>90% Vdd min</td> <td>2.4V min</td> </tr> <tr> <td>Logic "0"</td> <td>10% Vdd max</td> <td>0.4V min</td> </tr> <tr> <td>Load</td> <td>15pF</td> <td>10TTL</td> </tr> <tr> <td>Duty Cycle</td> <td>40/60</td> <td>40/60</td> </tr> <tr> <td>Rise & Fall</td> <td>10nS max</td> <td>10nS max</td> </tr> </tbody> </table>			HCMOS	TTL	Logic "1"	90% Vdd min	2.4V min	Logic "0"	10% Vdd max	0.4V min	Load	15pF	10TTL	Duty Cycle	40/60	40/60	Rise & Fall	10nS max	10nS max										
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	<p>Phase Noise (typical)</p> <p>20MHz offset</p> <ul style="list-style-type: none"> -80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @100KHz 																													
	<p>Frequency Adjustment ±3ppm min by internal trimmer</p>																													
	ENVIROMENTAL & MECHANICAL SPECIFICATION																													
	<p>Shock MIL-STD-883C, Method 2002, Condition B Vibration MIL-STD-883C, Method 2007, Condition A Solderability MIL-STD-883C, Method 2003 Seal integrity MIL-STD-883C, Method 1014, Condition C & A2 Marking MIL-STD-202F, Method 215</p>																													
<p>TEST CIRCUIT</p> 	<p>TABLE1</p> <table border="1"> <thead> <tr> <th>Symbol</th> <th>Stability</th> </tr> </thead> <tbody> <tr><td>05</td><td>±0.5ppm</td></tr> <tr><td>10</td><td>±1.0ppm</td></tr> <tr><td>15</td><td>±1.5ppm</td></tr> <tr><td>20</td><td>±2.0ppm</td></tr> <tr><td>25</td><td>±2.5ppm</td></tr> <tr><td>30</td><td>±3.0ppm</td></tr> <tr><td>35</td><td>±3.5ppm</td></tr> <tr><td>50</td><td>±5.0ppm</td></tr> </tbody> </table>		Symbol	Stability	05	±0.5ppm	10	±1.0ppm	15	±1.5ppm	20	±2.0ppm	25	±2.5ppm	30	±3.0ppm	35	±3.5ppm	50	±5.0ppm										
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