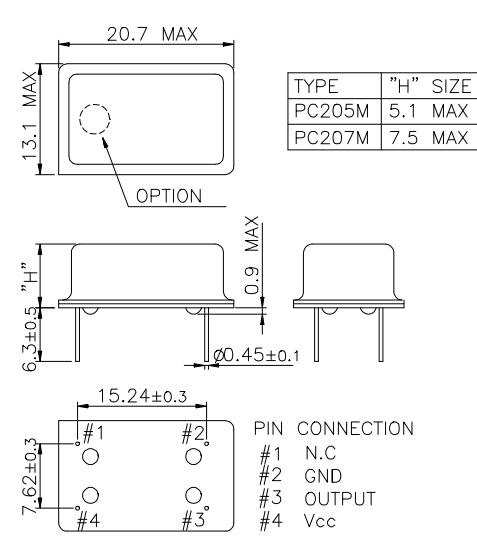
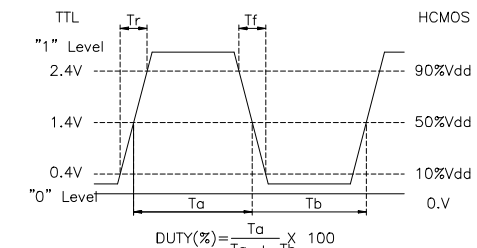
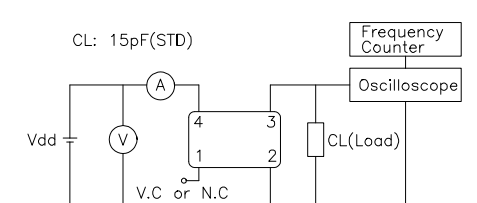


MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION																																														
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<p>Frequency Stability</p> <p>vs. Temperature vs. Supply Voltage vs. Load vs. Aging</p>	<p>±0.5 ppm to ±5.0ppm ±0.1 / ±0.3 ppm max / Vdd ± 5% ±0.2 ppm max /15pF ±10% ±1.0 ppm max/ year</p>																																														
<p>Temperature Range</p> <p>Operating Storage</p>	<p>See Table 2 -55°C to 125°C</p>																																														
<p>Supply Voltage</p>	<p>3.3V ± 5% 5.0V ± 5%</p>																																														
<p>Input Current</p> <p>3.3 V , 5V</p>	<p>1.000KHz ~ 40.000MHz ~ 800.000MHz 15mA max ~ 30mA max ~ 50mA max</p>																																														
<p>Output characteristics</p>	<table border="1"> <thead> <tr> <th></th> <th>HCMOS</th> <th>TTL</th> </tr> </thead> <tbody> <tr> <td>Logic "1"</td> <td>90% Vdd min</td> <td>2.4V min</td> </tr> <tr> <td>Logic "0"</td> <td>10% Vdd max</td> <td>0.4V min</td> </tr> <tr> <td>Load</td> <td>15pF</td> <td>10TTL</td> </tr> <tr> <td>Duty Cycle</td> <td>40/60</td> <td>40/60</td> </tr> <tr> <td>Rise & Fall</td> <td>10nS max</td> <td>10nS max</td> </tr> </tbody> </table>		HCMOS	TTL	Logic "1"	90% Vdd min	2.4V min	Logic "0"	10% Vdd max	0.4V min	Load	15pF	10TTL	Duty Cycle	40/60	40/60	Rise & Fall	10nS max	10nS max																												
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<p>Phase Noise (typical)</p> <p>20MHz offset</p>	<p>-80 dBc / Hz @ 10Hz -120 dBc / Hz @ 100Hz -135 dBc / Hz @ 1KHz -140 dBc / Hz @ 10KHz -145 dBc / Hz @100KHz</p>																																														
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<p>Shock Vibration Solderability Seal integrity Marking</p>	<p>MIL-STD-883C, Method 2002, Condition B MIL-STD-883C, Method 2007, Condition A MIL-STD-883C, Method 2003 MIL-STD-883C, Method 1014, Condition C & A2 MIL-STD-202F, Method 215</p>																																														
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