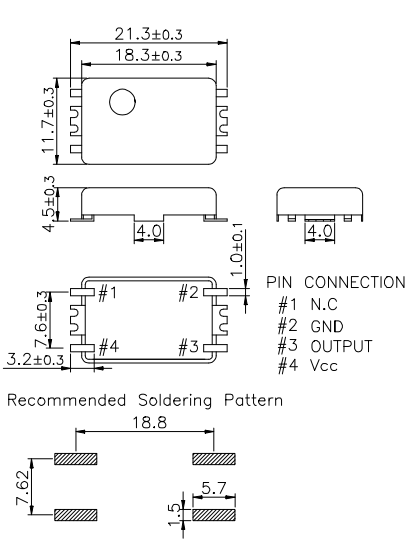
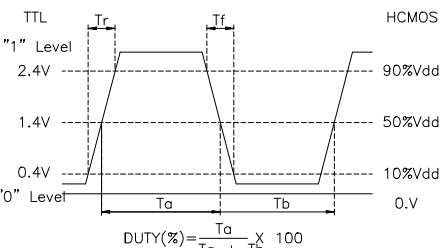
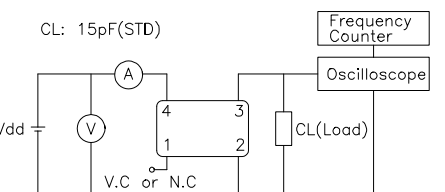


MECHANICAL DIMENSIONS	ELECTRICAL SPECIFICATION																																															
 <p>PIN CONNECTION            #1 N.C            #2 GND            #3 OUTPUT            #4 Vcc</p> <p>Recommended Soldering Pattern</p>	<p>Frequency range</p>	<p>1.000KHz to 800.000MHz            All combination of Frequency range Vs. Package type might not be available ,please contact factory.</p>																																														
	<p>Frequency Stability            vs. Temperature            vs. Supply Voltage            vs. Load            vs. Aging</p>	<p><math>\pm 0.5</math> ppm to <math>\pm 5.0</math>ppm  <math>\pm 0.1 / \pm 0.3</math> ppm max / Vdd <math>\pm 5\%</math>  <math>\pm 0.2</math> ppm max /15pF <math>\pm 10\%</math>  <math>\pm 1.0</math> ppm max/ year</p>																																														
	<p>Temperature Range            Operating            Storage</p>	<p>See Table 2  <math>-55^{\circ}\text{C}</math> to <math>125^{\circ}\text{C}</math></p>																																														
	<p>Supply Voltage</p>	<p><math>3.3\text{V} \pm 5\%</math>  <math>5.0\text{V} \pm 5\%</math></p>																																														
	<p>Input Current            3.3 V , 5V</p>	<p>1.000KHz ~ 40.000MHz ~ 800.000MHz            15mA max ~ 30mA max ~ 50mA max</p>																																														
<p>OUTPUT WAVEFORM</p>  <p>DUTY(%) = <math>\frac{T_a}{T_a + T_b} \times 100</math></p>	<p>Output characteristics</p>	<table border="1"> <thead> <tr> <th></th> <th>HCMOS</th> <th>TTL</th> </tr> </thead> <tbody> <tr> <td>Logic "1"</td> <td>90% Vdd min</td> <td>2.4V min</td> </tr> <tr> <td>Logic "0"</td> <td>10% Vdd max</td> <td>0.4V min</td> </tr> <tr> <td>Load</td> <td>15pF</td> <td>10TTL</td> </tr> <tr> <td>Duty Cycle</td> <td>40/60</td> <td>40/60</td> </tr> <tr> <td>Rise &amp; Fall</td> <td>10nS max</td> <td>10nS max</td> </tr> </tbody> </table>		HCMOS	TTL	Logic "1"	90% Vdd min	2.4V min	Logic "0"	10% Vdd max	0.4V min	Load	15pF	10TTL	Duty Cycle	40/60	40/60	Rise & Fall	10nS max	10nS max																												
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	<p>Phase Noise (typical)            20MHz offset</p>	<p><math>-80</math> dBc / Hz @ 10Hz  <math>-120</math> dBc / Hz @ 100Hz  <math>-135</math> dBc / Hz @ 1KHz  <math>-140</math> dBc / Hz @ 10KHz  <math>-145</math> dBc / Hz @100KHz</p>																																														
	<p>Frequency Adjustment</p>	<p><math>\pm 3</math>ppm min by internal trimmer</p>																																														
<p>TEST CIRCUIT</p> 	<p>ENVIROMENTAL &amp; MECHANICAL SPECIFICATION</p>																																															
	<p>Shock            Vibration            Solderability            Seal integrity            Marking</p>	<p>MIL-STD-883C, Method 2002, Condition B            MIL-STD-883C, Method 2007, Condition A            MIL-STD-883C, Method 2003            MIL-STD-883C, Method 1014, Condition C &amp; A2            MIL-STD-202F, Method 215</p>																																														
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